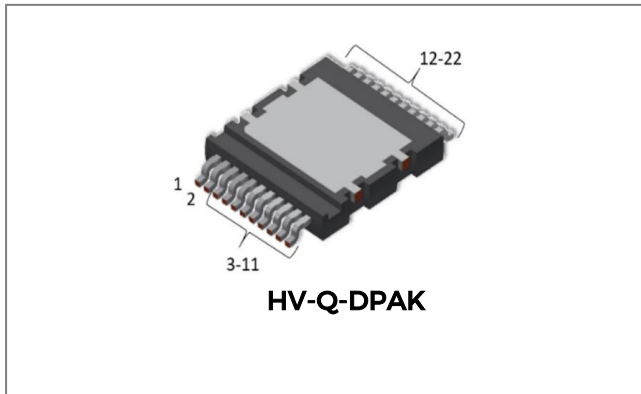


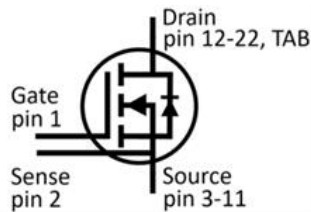
S3M0030120Q Silicon Carbide Power MOSFET



Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance typ. $R_{DS(on)} = 30 \text{ m}\Omega$.
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright tin electroplatin.
- "-A" is an AEC-Q101 qualified device.

Circuit Diagram



Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

Maximum Ratings($T_c = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Typ.	Max.	Units	Note
Drain - Source Voltage	V_{DSmax}	$V_{GS} = 0 \text{ V}$, $I_D = 100 \mu\text{A}$			1200	V	
Gate - Source Voltage (dynamic)	V_{GSmax}	AC ($f > 1 \text{ Hz}$)	-8		+22	V	
Gate - Source Voltage (static)	V_{GSop}	Static		-4 / +18		V	[1]
Continuous Drain Current	I_D	$V_{GS} = 18 \text{ V}$, $T_c = 25^\circ\text{C}$			73	A	
		$V_{GS} = 18 \text{ V}$, $T_c = 100^\circ\text{C}$			52		
Pulsed Drain Current	$I_{D(pulse)}$	Pulse width t_p limited by T_{jmax}			223	A	
Power Dissipation	P_D	$T_c = 25^\circ\text{C}$			404	W	

[1] Recommended turn off gate voltage is -4 V. Recommended turn on gate voltage is 18 V. Do not use with $V_{GSON} < 12 \text{ V}$.

Electrical Characteristics:

Characteristics	Symbol	Conditions	Min.	Typ.	Max.	Units
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	1200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 16\text{ mA}$	2	2.5	4	V
		$V_{DS} = V_{GS}, I_D = 16\text{ mA}, T_J = 175\text{ }^\circ\text{C}$		1.7		V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}$		1	100	μA
Gate Source Leakage Current	I_{CSS}	$V_{GS} = 18\text{ V}, V_{DS} = 0\text{ V}$		10	250	nA
Drain Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 18\text{ V}, I_D = 40\text{ A}$		30	39	m Ω
		$V_{GS} = 18\text{ V}, I_D = 40\text{ A}, T_J = 175\text{ }^\circ\text{C}$		50		m Ω
Transconductance	gfs	$V_{DS} = 20\text{ V}, I_{DS} = 40\text{ A}$		18		S
		$V_{DS} = 20\text{ V}, I_{DS} = 40\text{ A}, T_J = 175\text{ }^\circ\text{C}$		19		S
Input Capacitance	C_{ISS}	$V_{GS} = 0\text{ V}$		2844		pF
Output Capacitance	C_{OSS}	$V_{DS} = 1000\text{ V}$		134		
Reverse Transfer Capacitance	C_{RSS}	$V_{AC} = 25\text{ mV}$		17		
COSS Stored Energy	E_{OSS}	$f = 1\text{ MHz}$		78		
Turn-On Switching Energy	E_{ON}	$V_{DS} = 800\text{ V}, V_{GS} = -4 / 18\text{ V}$		212		μJ
Turn-Off Switching Energy	E_{OFF}	$I_D = 40\text{ A}, R_{C(ext)} = 2.5\text{ }\Omega, L = 99\text{ }\mu\text{H}$		198		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 800\text{ V}, V_{GS} = -4 / 18\text{ V}$		12.5		ns
Rise Time	t_r	$I_D = 40\text{ A}, R_{C(ext)} = 2.5\text{ }\Omega$		14.7		
Turn-Off Delay Time	$t_{d(off)}$	Inductive Load Timing relative to		27.5		
Fall Time	t_f	v_{DS} Per IEC60747-8-4 pg 83		7.0		
Internal Gate Resistance	$R_{G(int)}$	$f = 1\text{ MHz}, AC = 25\text{ mV}$		1.3		Ω
Gate to Source Charge	Q_{gs}	$V_{DS} = 800\text{ V}, V_{GS} = -4 / 18\text{ V}$		66		nC
Gate to Drain Charge	Q_{gd}	$I_D = 40\text{ A}$		49		
Total Gate Charge	Q_g	Per IEC60747-8-4 pg 21		143		

Reverse Diode Characteristics (TA = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Typ.	Max.	Units
Diode Forward Voltage	V _{SD}	V _{GS} = -4 V, I _{SD} = 20 A	4.3		V
	V _{SD}	V _{GS} = -4 V, I _{SD} = 20 A, T _J = 175 °C	3.6		V
Continuous Diode Forward Current	I _S	V _{GS} = -4 V, T _C = 25 °C	48		A
Reverse Recovery Time	t _{rr}	V _{GS} = -4 V, I _{SD} = 40 A, T _J = 25 °C V _R = 800 V dif / dt = 2000 A / μs	16		ns
Reverse Recovery Charge	Q _{rr}		221		nC
Peak Reverse Recovery Current	I _{mm}		23		A

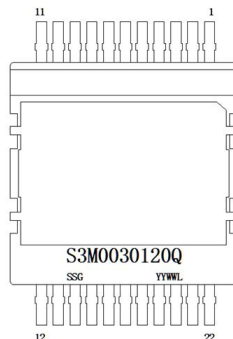
Thermal-Mechanical Specifications

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T _J	-	-55 to +175	°C
Storage Temperature	T _{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R _{θJC}		0.37	°C /W

Ordering Information

Device	Package	Shipping
S3M0030120Q	HV-Q-DPAK	1000 pcs/reel

Marking Diagram



S3M = Device Type
 0030 = RDS(on)
 120 = Reverse Voltage (1200V)
 Q = Package
 SSG = SSG
 YY = Year
 WW = Week
 L = Lot Number

Cautions: Molding resin
 Epoxy resin UL94V-0

Ratings and Characteristics Curves

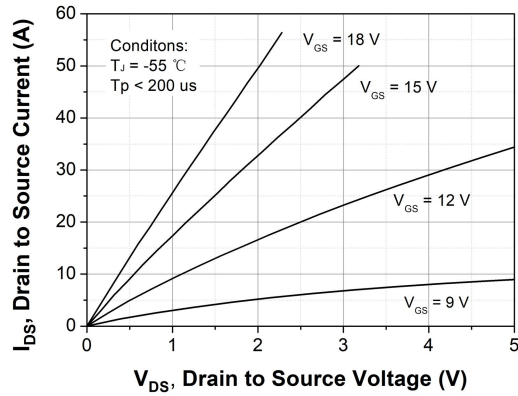


Figure 1. Output Characteristics $T_J = -55^\circ\text{C}$

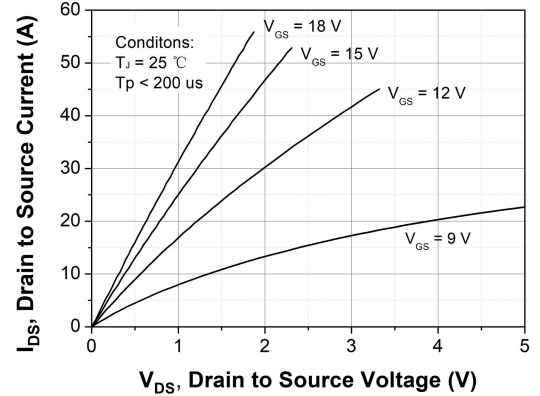


Figure 2. Output Characteristics $T_J = 25^\circ\text{C}$

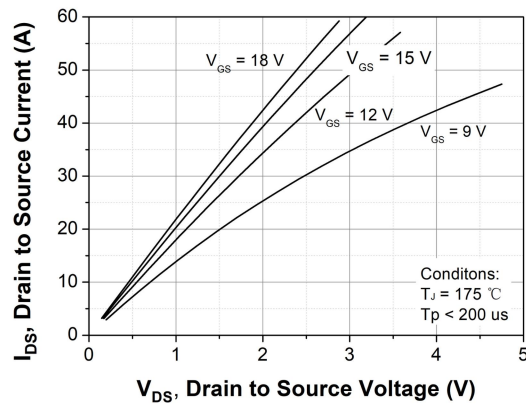


Figure 3. Output Characteristics $T_J = 175^\circ\text{C}$

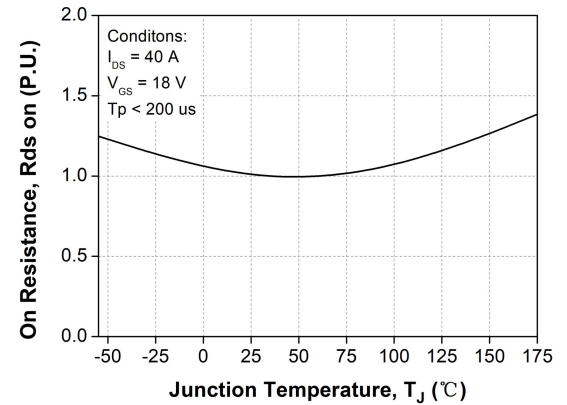


Figure 4. Normalized On-Resistance vs. Temperature

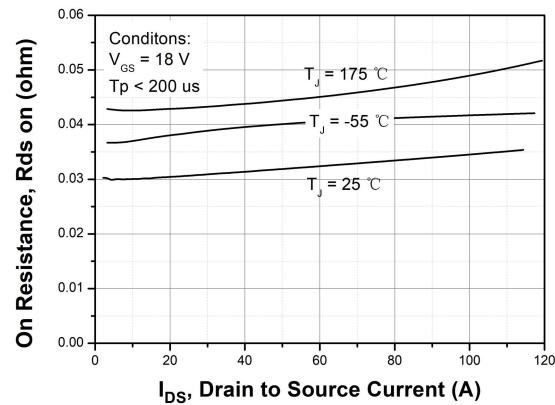


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

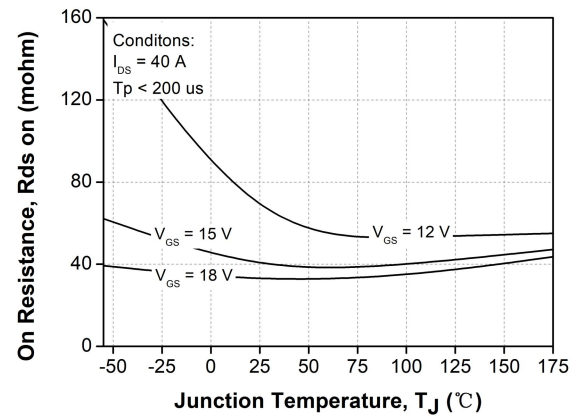


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

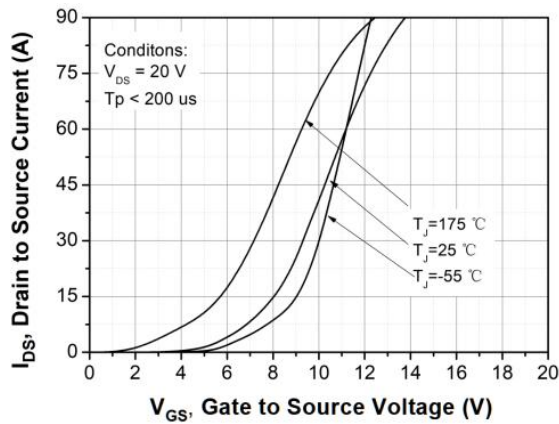


Figure 7. Transfer Characteristic for Various Junction Temperatures

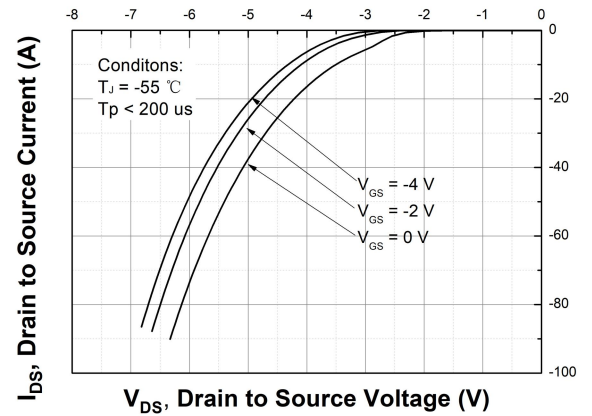


Figure 8. Body Diode Characteristic at $T_J = -55\text{ }^{\circ}\text{C}$

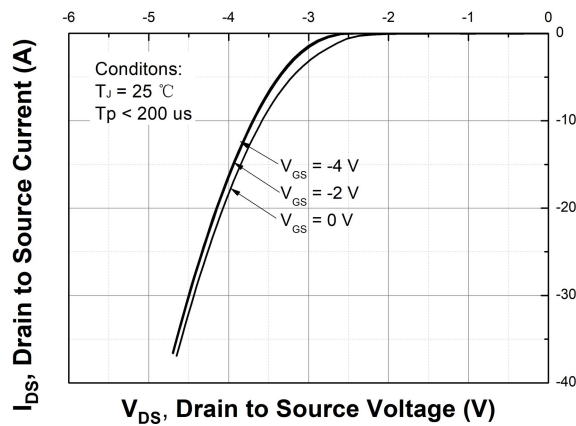


Figure 9. Body Diode Characteristic at $T_J = 25\text{ }^{\circ}\text{C}$

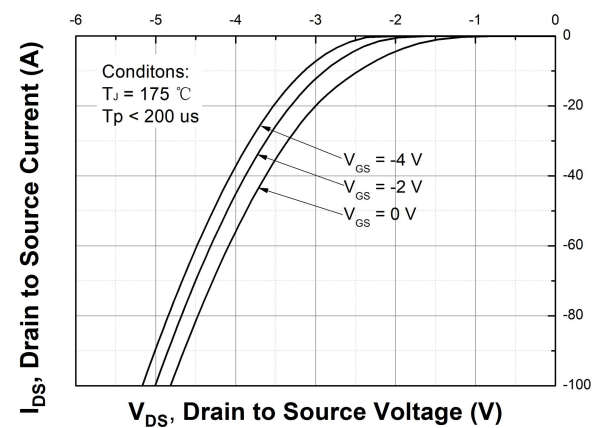


Figure 10. Body Diode Characteristic at $T_J = 175\text{ }^{\circ}\text{C}$

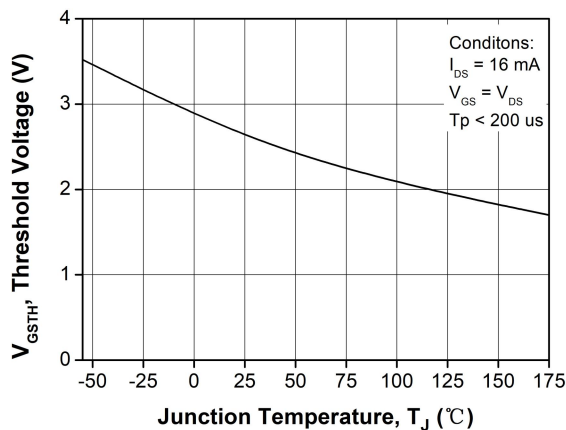


Figure 11. Threshold Voltage vs. Temperature

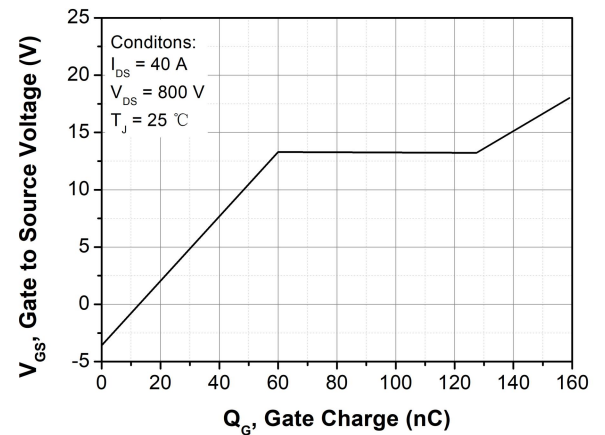


Figure 12. Gate Charge Characteristic

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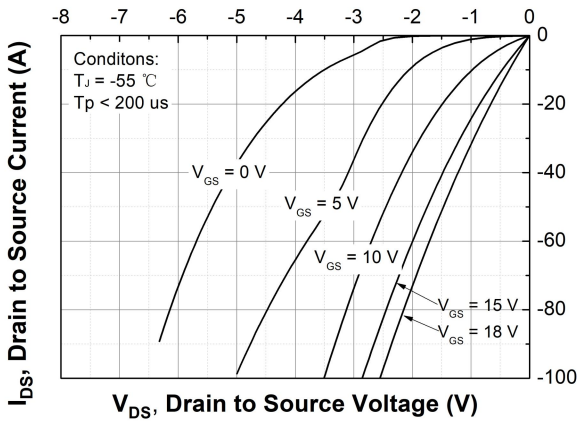


Figure 13. 3rd Quadrant Characteristic at $T_j = -55\text{ }^{\circ}\text{C}$

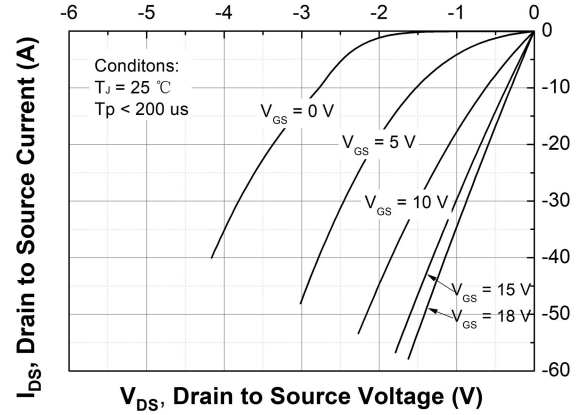


Figure 14. 3rd Quadrant Characteristic at $T_j = 25\text{ }^{\circ}\text{C}$

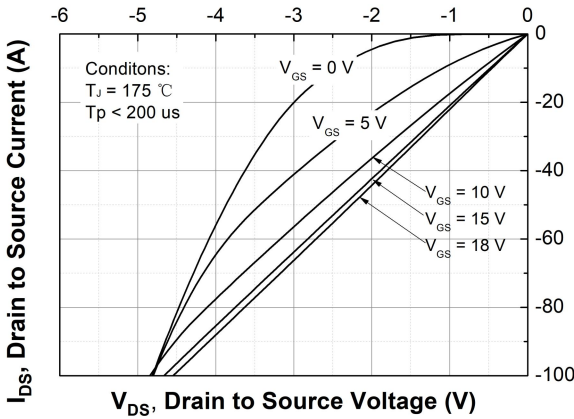


Figure 15. 3rd Quadrant Characteristic at $T_j = 175\text{ }^{\circ}\text{C}$

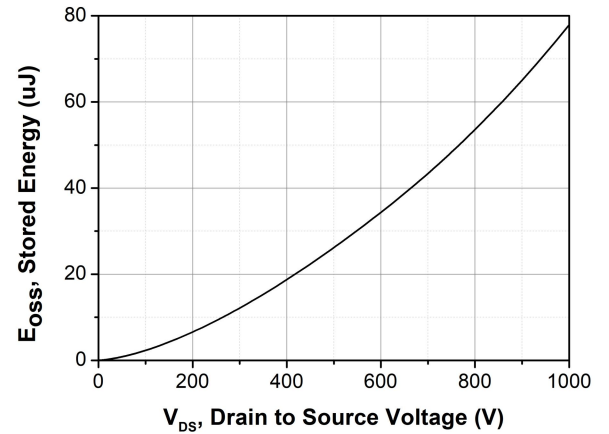


Figure 16. Output Capacitor Stored Energy

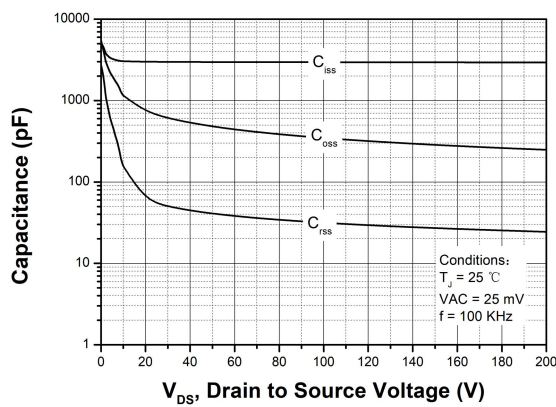


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200 V)

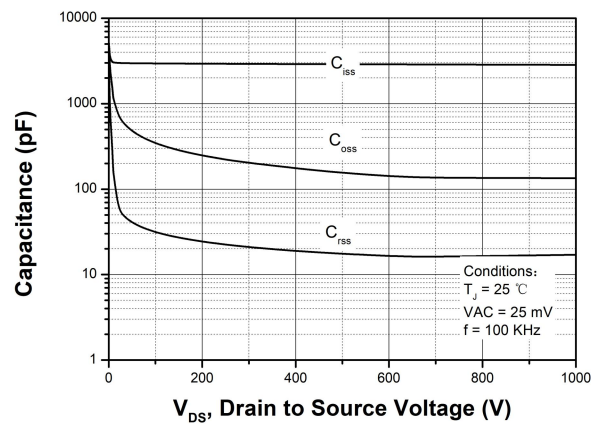


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

Technical Data

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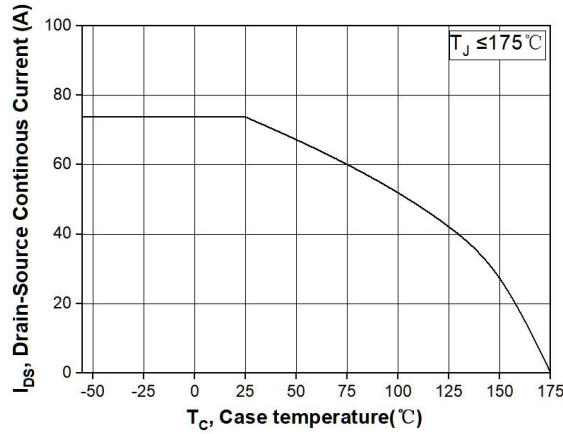


Figure 19. Continuous Drain Current Derating vs. Case Temperature

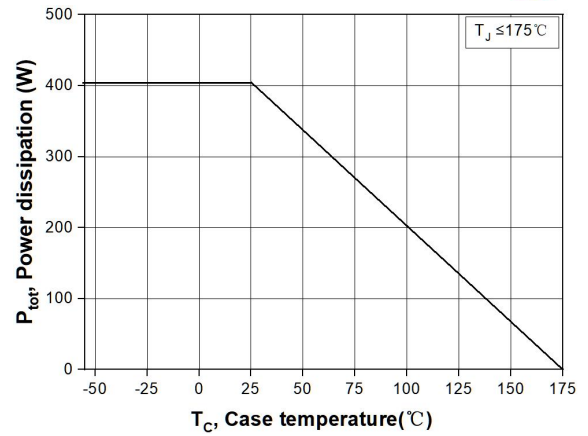


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

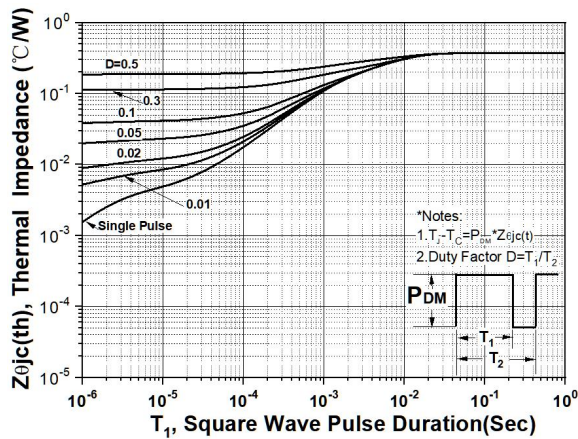


Figure 21. Transient Thermal Impedance (Junction - Case)

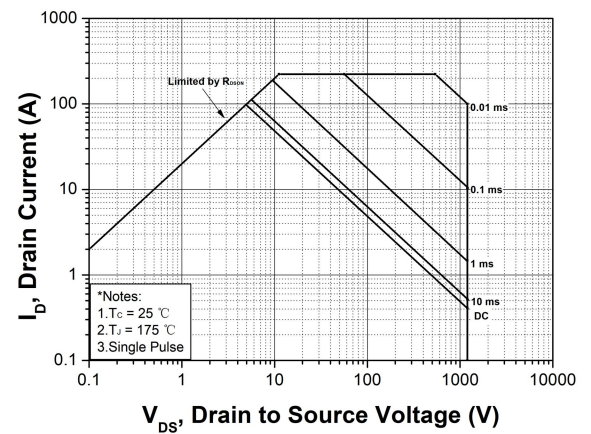


Figure 22. Safe Operating Area

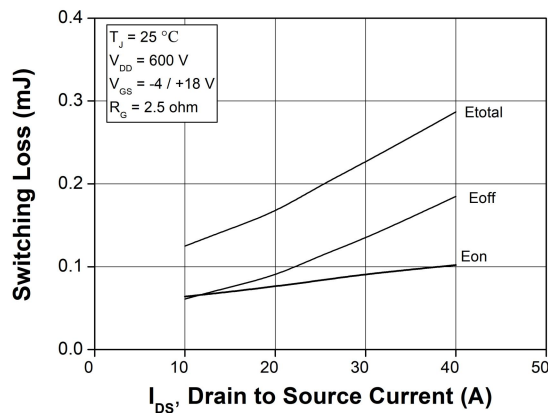


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600V$)

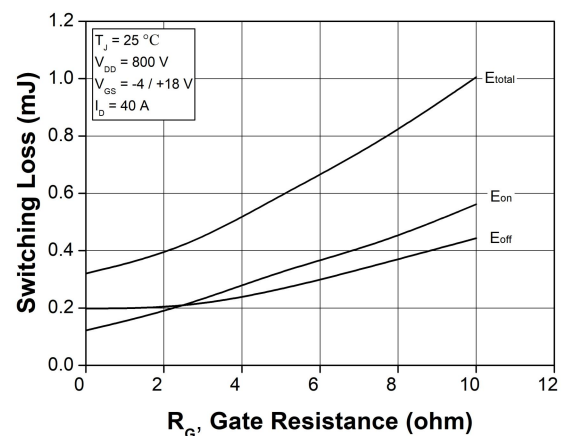


Figure 24. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

Technical Data

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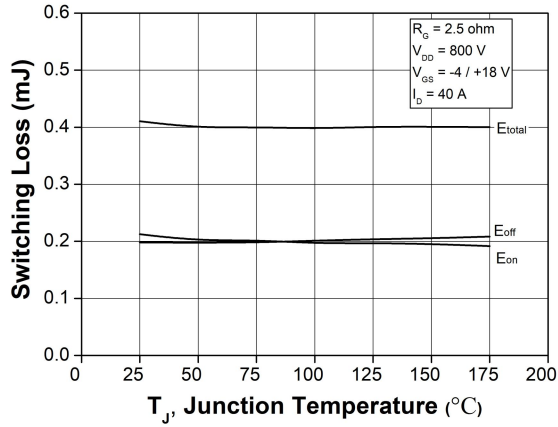


Figure 25. Clamped Inductive Switching Energy vs. Temperature

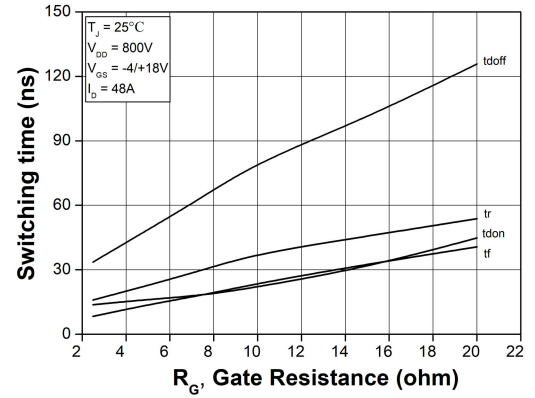


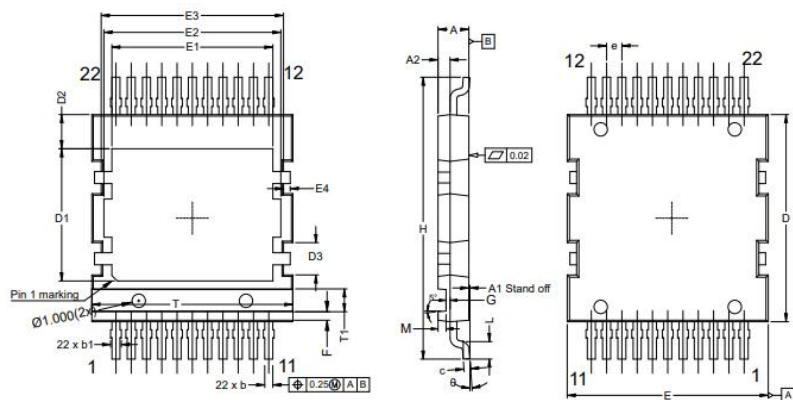
Figure 26. Switching Times vs. $R_{G(ext)}$

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Mechanical Dimensions HV-Q-DPAK



The outline from different package houses may have slight differences. So the outline above is just schematic. The dimensions are controlled per specifications.

COMMON DIMENSIONS		
SYMBOL	mm	
	Min	Max
A	2.20	2.35
A1	0.00	0.15
A2	0.80	1.00
b	0.50	0.70
b1	0.50	0.90
c	0.46	0.58
e	1.14	
D	15.30	15.50
D1	9.74	9.94
D2	2.54	
D3	2.40	
E	14.90	15.10
E1	11.91	12.11
E2	13.20	
E3	13.60	
E4	0.50	
H	20.81	21.11
L	1.20	1.40
T	14.90	15.10
T1	1.65	1.75
M	0.60	
F	0.631	
G	0.30	

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